ECR #: P2
Tracker #: 4
Status: Ratified
Title: Correction to Figure 3.18
Release Date: June 2, 1997
Impact: Changes the Nature of the I/O Aperture Design
Spec Version: NLX 1.2

Summary:

In Figure 3.18, clarify the wording about the flanges on the I/O aperture to ensure that a flange exists such that a standard NLX I/O shield can be used. In the figure, change the callout "Maximum Top Flange" to "Maximum Top Flange Gap," and change "Minimum Lower Flange" to "Minimum Lower Flange Gap." There are no dimensional changes.

Background:

The intent of the specification is to ensure that a flange exists around the I/O aperture such that it allows for the outer perimeter of the I/O shield to make EMI contact.

1. The measurement between the end of the top flange and the outside perimeter of the motherboard should be called out as a maximum top flange gap.

2. The measurement between the end of the bottom flange and the outside perimeter of the motherboard should be called out as a minimum lower flange gap.

Change Current Specification As Shown:

Replace Figure 3.18 with the following diagram.

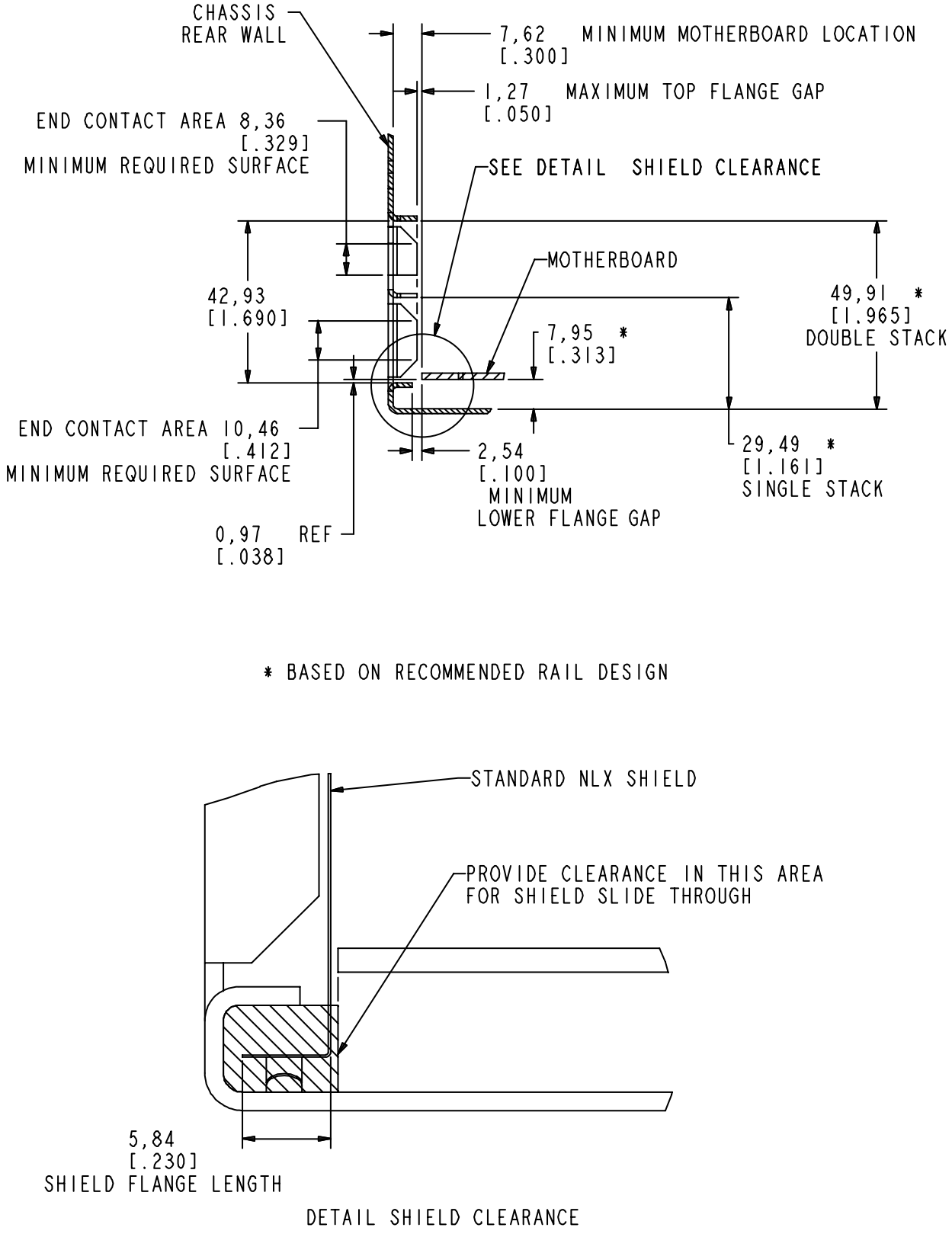

Figure 3.18: Side View of Back Panel Double High I/O Shield Opening Dimensions